# Synthesis of minimal cost nonlinear feedback shift registers 

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#### Abstract

This paper presents a block type algorithm which determines the "minimal" nonlinear feedback shift register (NLFSR) that generates a given sequence. Minimality is defined with respect to a total ordering between structural vectors that takes into account the implementation cost.


## 1 Introduction

In this paper a block type algorithm is developed which determines the minimal NLFSR that generates a given finite sequence with elements in a field $F$. The proposed algorithm combines a total ordering, a linear dependence test and a minimality test. The total ordering facilitates the evaluation of the NLFSR. It is necessitated by the fact that each NLFSR is characterized by a structural multiindex representing the order of nonlinearities, the main signal products (called primary signals) and the maximum delays. The coefficients of a NLFSR producing the given signal live in the null space of a suitable matrix. Hence, each NLFSR amounts to expressing a column of the above matrix as a linear combination of preceding columns. A special feature of this matrix is that the lengths of its columns are not the same. This complication requires a more careful analysis of linear dependence. The linear dependence test achieves the above goal via manipulation of column subblocks and a proper adaptation of the FIA algorithm [2]. The third ingredient of the algorithm matches the linear dependence test with the total ordering. In this manner it eventually determines the NLFSR that is minimal with respect to the above ordering.

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## 2 Regular representation

Suppose that the given sequence $x(n), 1 \leq n \leq N$ is generated by the following nonlinear source:

$$
\begin{align*}
& x(n)=\sum_{i=1}^{L} c_{i} x(n-i)+ \\
&+ \sum_{i_{1}=0}^{p_{22}} \sum_{i=1}^{L_{i_{1}}} c_{i, i_{1}} x(n-i) x\left(n-i-i_{1}\right)+\cdots+ \\
&+\sum_{i_{1}=0}^{p_{k 2}} \cdots \sum_{i_{k-1}=0}^{p_{k k}-\sum_{l=1}^{k-2} i_{i} \sum_{i=1}^{L_{i_{1}}, \cdots, i_{k-1}} c_{i, i_{1}, \cdots, i_{k-1}} x(n-i) .} \\
& \quad \cdot \prod_{j=1}^{k-1} x\left(n-i-\sum_{w=1}^{j} i_{w}\right), n>M \tag{1}
\end{align*}
$$

In the so-called regular representation (1) which defines a recursively computable expression, $k$ denotes the maximum order (degree) of nonlinearity [4]. The linear part is characterized by its order $L$ and the corresponding coefficients $c_{i}, 1 \leq$ $i \leq L$. The second order part is parameterized by the orders $p_{22}$ and $L_{i_{1}}$ and the quadratic coefficients $c_{i, i_{1}}, 1 \leq i \leq L_{i_{1}}, 0 \leq i_{1} \leq p_{22}$. The higher order terms are similarly defined with $p_{m 2} \leq$ $p_{m 3} \leq \cdots \leq p_{m m}, 3 \leq m \leq k$. The parameters $L, L_{i_{1}}, \cdots, p_{22}, p_{32}, \cdots$ are nonnegative integers. The coefficients $c_{i}, c_{i, i_{1}}, \cdots, c_{i, i_{1}, \cdots, i_{k-1}}$ and the resulting signal values reside in a field $F$ and additions and multiplications are the field operations. Finally, parameter $M$ is related to the length of initial conditions of the NLFSR.

The structural parameters of (1) can be incorporated into the vector

$$
\begin{equation*}
u=(k, r(k), S(k, r(k))) \tag{2}
\end{equation*}
$$

where $r(k)=\left(\begin{array}{l|ll|l|ll}p_{22} & \mid & p_{32} & p_{33} & \cdots & p_{k 2}\end{array} \cdots\right)$ and $S(k, r(k))=\left(\begin{array}{l|lll}L \mid & L_{0} & \cdots & L_{p_{22}} \mid \\ L_{0,0} \cdots\end{array}\right)$.

Eq. (1) reveals that the output signal $x(n)$ is obtained by a linear combination of shifted copies of a set of so-called primary signals [3]. The number of primary signals associated with a specific nonlinearity of order $m$ is determined by the parameters


Figure 1: The architecture of NLFSR implementation
$\left\{p_{m l}\right\}, l=2, \cdots, m$, while the shifts involved in a specific primary signal determined by the indices $i_{1}, \cdots, i_{m-1}$ is indicated by $L_{i_{1}, \cdots, i_{m-1}}$. By convention $L_{i_{1}, \cdots, i_{m-1}}=0$ declares the absence of the corresponding primary signal.
The regular representation can be directly and canonically mapped onto an architecture where three main structural modules are involved, as indicated in Fig. 1. The primary signal development module generates the primary signals. It takes as inputs the delays of signal $x(n)$ and requires the use of multipliers for the generation of the primary signals. The delay module includes a delay structure for each primary signal that spans all pertinent signal products involved in eq. (1). The third module includes the scalors associated with the coefficients $c_{i}, c_{i, i_{1}}, \cdots$ and the appropriate adders that are combined to generate $x(n)$.
Eq. (1) is nonlinear with respect to $x(n)$ but is linear with respect to the unknown coefficients $\left\{c_{\left.i, i_{1}, \cdots, i_{m-1}\right\}}\right\}$. Collecting all signal samples $x(n)$, $M<n \leq N$ and setting $c_{0}=-1$, we write eq. (1) in matrix form as

$$
\begin{equation*}
T_{1}(u) \cdot C=0 \tag{3}
\end{equation*}
$$

where the $(N-M) \times Q$ matrix $T_{1}(u)$ and the $Q \times 1$ column vector $C$ are defined as:

$$
\begin{gathered}
T_{1}(u)=\left(\begin{array}{llllll}
X_{M-L}^{N-M} & X_{M-L+1}^{N-M} & \cdots & X_{M}^{N-M} & X_{M-L_{0}, 0}^{N-M} & \cdots
\end{array}\right) \\
C=\left(\begin{array}{llllll}
c_{L} & c_{L-1} & \cdots & c_{0} & c_{L_{0}, 0} & \cdots
\end{array}\right)^{T}
\end{gathered}
$$

and

$$
\begin{gathered}
X_{P}^{R}=\left(\begin{array}{lll}
x(P+1) & x(P+2) & \cdots
\end{array} x(P+R)\right)^{T}, \\
X_{P, i_{1}, \cdots, i_{m-1}}^{R}=\left(\begin{array}{c}
x(P+1) \cdot \prod_{w=1}^{m-1} x\left(P+1-\sum_{j=1}^{w} i_{j}\right) \\
x(P+2) \cdot \prod_{w=1}^{m-1} x\left(P+2-\sum_{j=1}^{w} i_{j}\right) \\
\vdots \\
x(P+R) \cdot \prod_{w=1}^{m-1} x\left(P+R-\sum_{j=1}^{w} i_{j}\right)
\end{array}\right)
\end{gathered}
$$

where $2 \leq m \leq k$ and $P \geq 0$ determines the first entry. Clearly $u$ controls the size of $T_{1}(u)$. Due to the shift invariance of eq. (4) we have for any $0 \leq$
$K \leq R, X_{P}^{R}=\left(X_{P}^{K^{T}} x(P+K+1) \cdots x(P+R)\right)^{T}$, where $X_{P}^{K}$ provides the upper subblock of $X_{P}^{R}$ of length $K$. Vector $X_{P, i_{1}, \cdots, i_{m-1}}^{R}$ is partitioned in a similar fashion.

It is useful to harmonize the column lengths by arbitrarily extending $T_{1}(u)$ to
$T(u)=\left(\begin{array}{llllll}X_{M-L}^{N} & X_{M-L+1}^{N} & \cdots & X_{M}^{N} & X_{M-L_{0}, 0}^{N} & \cdots\end{array}\right)$
where the signal values $x(n)$ that fall outside the given range $1 \leq n \leq N$ are arbitrarily assigned and are referred to as "don't care" entries.

## 3 Total ordering of regular forms

Let $V(u)$ be a real function $V(u): \mathcal{A} \rightarrow \mathcal{R}$ that represents the implementation cost associated with the architecture corresponding to $u$, where $\mathcal{A}$ includes all $u$ that satisfy problem requirements. $V(u)$ is termed the ordering function. Let

$$
\begin{aligned}
& \mathcal{B}(u)=\left(1 \quad \sum_{i_{1}} g_{s}\left(L_{i_{1}}\right) \cdots \sum_{i_{1}} \cdots \sum_{i_{k-1}} g_{s}\left(L_{i_{1}, \cdots, i_{k-1}}\right)\right)^{T} \\
& \mathcal{Q}(u)=\left(\begin{array}{llll}
L+1 & \sum_{i_{1}} L_{i_{1}} & \cdots & \left.\sum_{i_{1}} \cdots \sum_{i_{k-1}} L_{i_{1}, \cdots, i_{k-1}}\right)^{T}
\end{array}\right.
\end{aligned}
$$

where $g_{s}(n)$ equals zero for $n \leq 0$ and 1 otherwise. If $g_{s}\left(L_{i_{1}, \cdots, i_{m-1}}\right)=0$ the corresponding primary signal is absent. The $i$-th component of $\mathcal{B}(u)$ provides the number of primary signals associated with the order $i$. Similarly the entries of $\mathcal{Q}(u)$ describe the delays of the primary signals.

An ordering denoted by $<_{V}$ is defined as follows: Let $u_{1}=\left(k_{1}, r_{1}\left(k_{1}\right), S_{1}\left(k_{1}, r_{1}\left(k_{1}\right)\right)\right), u_{2}=$ $\left(k_{2}, r_{2}\left(k_{2}\right), S_{2}\left(k_{2}, r_{2}\left(k_{2}\right)\right)\right) \in \mathcal{A}$. Then
$u_{1}<_{V} u_{2}$ if and only if one of the following holds:

1. $V\left(u_{1}\right)<V\left(u_{2}\right)$
2. $V\left(u_{1}\right)=V\left(u_{2}\right)$ and $k_{1}<k_{2}$
3. $V\left(u_{1}\right)=V\left(u_{2}\right), k_{1}=k_{2}$ and $\mathcal{B}\left(u_{1}\right)<_{T} \mathcal{B}\left(u_{2}\right)$
4. $V\left(u_{1}\right)=V\left(u_{2}\right), k_{1}=k_{2}, \mathcal{B}\left(u_{1}\right)=\mathcal{B}\left(u_{2}\right)$ and $r_{1}\left(k_{1}\right)<{ }_{T} r_{2}\left(k_{2}\right)$
5. $V\left(u_{1}\right)=V\left(u_{2}\right), k_{1}=k_{2}, \mathcal{B}\left(u_{1}\right)=\mathcal{B}\left(u_{2}\right)$, $r_{1}\left(k_{1}\right)=r_{2}\left(k_{2}\right)$ and $\mathcal{Q}\left(u_{1}\right)<_{T} \mathcal{Q}\left(u_{2}\right)$
6. $V\left(u_{1}\right)=V\left(u_{2}\right), k_{1}=k_{2}, \mathcal{B}\left(u_{1}\right)=\mathcal{B}\left(u_{2}\right)$, $r_{1}\left(k_{1}\right)=r_{2}\left(k_{2}\right), \quad \mathcal{Q}\left(u_{1}\right)=\mathcal{Q}\left(u_{2}\right)$ and $S\left(k_{1}, r_{1}\left(k_{1}\right)\right)<_{T} S\left(k_{2}, r_{2}\left(k_{2}\right)\right)$
where the total degree ordering between two $m \times 1$ vectors $p$ and $q$ is defined by :
$\left(\begin{array}{lllll}p_{1} & \cdots & p_{i} & \cdots & p_{m}\end{array}\right)<_{T}\left(\begin{array}{lllll}q_{1} & \cdots & q_{i} & \cdots & q_{m}\end{array}\right)$
iff either $\sum_{1 \leq i \leq m} p_{i}<\sum_{1 \leq i \leq m} q_{i}$ or $\left(\sum_{1 \leq i \leq m} p_{i}=\sum_{1 \leq i \leq m} q_{i}\right)$
$\wedge(\exists i)\left((1 \leq i \leq m) \wedge\left(p_{i}<q_{i}, p_{i+1}=q_{i+1}, \cdots, p_{m}=q_{m}\right)\right)$
The ordering $<_{V}$ is a total ordering and is mainly based on the cost function $V(u)$. For equal cost implementations the ordering $<_{V}$ is based on the degree of nonlinearity. If equality sustains the ordering $<_{V}$ relies on the number of primary signals or the number of delays. $V(u)$ can be assessed by summing the cost of scalors, adders, multipliers and delay units and has the property that adding extra feedback taps or inserting extra primary signals to the register increases its cost.

The problem under consideration is formulated as follows: Given a sequence $\{x(n)\}$ of length $N$ in the field $F$, a set of constraints and a cost function $V$, we search for $u=(k, r(k), S(k, r(k))) \in \mathcal{A}$ and $C \neq 0$ such that $T_{1}(u) \cdot C=0, C$ is recursively computable and for all $u^{\prime} \in \mathcal{A}$ with $u^{\prime}<_{V} u$ there exists no recursively computable $C^{\prime} \neq 0$ such that $T_{1}\left(u^{\prime}\right) \cdot C^{\prime}=0$.

A first approach in dealing with the problem is exhaustive search. Indeed, by the use of eq. (5) let us order the structural vectors as $u_{1}<_{V} u_{2}<_{V}$ $\cdots<_{V} u_{i}<_{V} u_{i+1}<_{V} \cdots$. Then exhaustive search successively constructs $T_{1}\left(u_{i}\right)$ and checks for recursively computable $C \neq 0$ such that $T_{1}\left(u_{i}\right) \cdot C=0$. This scheme involves an enormous number of calculations since the procedure for linear dependence is repeated for each $T_{1}\left(u_{i}\right)$.
An alternative is the insertion of all $T\left(u_{j}\right), u_{j}<_{V}$ $u_{i}$ in a matrix $T_{\max }\left(u_{i}\right)$ whose linear dependence eventually reflects the linear dependence of the $\operatorname{minimal} T_{1}\left(u_{i}\right)$. However, the resulting matrix contains a lot of "don't care" elements and the issue of linear dependence needs proper modification.

## 4 The proposed algorithm

The organization of the proposed algorithmic scheme is illustrated in Fig. 2. It consists of the preprocessing module and the main module. The preprocessing module builds the total ordering of the structural vectors contained in $\mathcal{A}$. This module
does not depend on the given sequence and provides the framework for the main module. It can be implemented in an off-line fashion.

The main module includes three submodules. The first submodule builds column by column a matrix $T_{\text {max }}$ that traces the ordering of structural vectors. The second submodule identifies a linear combiner (if any) of subcolumns of $T_{\max }$ that forms a candidate for a minimal solution. The third submodule checks if the linear combiner corresponds to the minimal solution.

More specifically, the first submodule follows the total ordering and updates the matrix $T_{\max }\left(u_{i}\right)$ by adding the column $X_{I_{i}}^{N}$ (if any) of $T\left(u_{i}\right)$ not included in $T_{\max }\left(u_{i-1}\right)$. By definition of total ordering and the architecture based on primary signals, it is obvious that every matrix $T\left(u_{i}\right), i>1$ contains at most one column not contained in any $T\left(u_{w}\right), w<i$. This column may correspond either to a new primary signal not contained in any $T\left(u_{w}\right), w<i$ or to the next shift of an existing primary signal. Thus all except possibly one column are contained in a preceding matrix $T_{\max }\left(u_{i-1}\right)$. Then the addresses of all columns of $T\left(u_{i}\right)$ included in $T_{\max }\left(u_{i}\right)$ are determined. This task is performed by the column addressing box of Fig. 2.

The second submodule estimates the discrepancy detector $f_{s}$ and the maximum length $l_{s}$ of the upper subblock of column $X_{I_{i}}^{N}$ that is linearly dependent on the corresponding subblocks of the previous columns of $T_{\max }\left(u_{i}\right)$, under the constraint that if a previous subblock contains a "don't care" entry, the corresponding tap is zero. The discrepancy detector of a column checks if the maximum linear combiner is applicable to the next row. This requires that all "don't care" entries in this row correspond to zero coefficients of the linear combiner. The discrepancy detector takes the value -1 (if combiner is not applicable) and 1 . If all columns of $T\left(u_{i}\right)$ are included in $T_{\max }\left(u_{i-1}\right)$, their characteristics $f_{s}$ and $l_{s}$ have already been determined in a previous step. Estimation of $l_{s}$ and $f_{s}$ is realized via a modified version of FIA [2] (EFIA) taking into consideration that the "don't care" entries are located successively at the bottom end of the columns.
The features $l_{s}$ and $f_{s}$ critically affect subsequent algorithmic steps. If for $X_{I_{i}}^{N}$ holds that $f_{s}=1$ and $l_{s}<N_{i}$, then the algorithm proceeds to the first submodule. ( $X_{I_{i}}^{N_{i}}$ denotes the upper subblock of $X_{I_{i}}^{N}$ that belongs to $\left.T_{1}\left(u_{i}\right)\right)$. Otherwise the third


Figure 2: The structure of the proposed algorithm
submodule checks if the obtained maximum linear combiner corresponds to the desired minimal solution. If this is not the case the algorithm proceeds to the first submodule. If that is the case then $u_{i}$ is the minimal solution, since as the algorithm proceeds sequentially according to the total ordering, at step $i$ all solutions $u<_{V} u_{i}$ have been rejected. Scaling and shifting leads to the minimal cost NLFSR.

## Complexity analysis

The exhaustive search involves the application of the linear dependence test on a set of matrices (matrix by matrix), while the proposed algorithm mainly involves the application of the linear dependence test on a single matrix (column by column) until the minimal NLFSR is reached. Suppose that $u_{b}$ is the structural vector of the minimal system generating the terms of the given sequence. Then, the proposed algorithm applies the linear dependence test on a $N \times q$ matrix, $q \leq b+1$ columns. If the exhaustive search is applied the solution is obtained through the application of FIA on $b$ matrices of size $N \times q_{i}, i=1, \cdots, b$. The reduction is obvious since $\sum_{i=1}^{b} q_{i}>q$.

## Example

Consider the finite field $G F\left(2^{4}\right)$ generated by $p(x)=1+x+x^{4}$, a primitive element $a$ and the sequence $a^{7}, a^{5}, 0, a^{10}, a^{7}, a^{4}, a^{3}$. Application of the algorithm for structural vectors $u \in \mathcal{A}$ with $k \leq 4$, $L \leq 10$ and $L_{i_{1}, \cdots, i_{m-1}} \leq 10,2 \leq m \leq 4$, provides the following minimal NLFSR

$$
x(n)=a^{2}(x(n-1))^{2}+a^{5} x(n-2), \quad n>2
$$

which indeed generates the given sequence. The same sequence is generated by the linear equation $x(n)=a^{10} x(n-1)+a x(n-2)+a^{7} x(n-3)+a^{9} x(n-4)$, $n \geq 5$. This linear register would be obtained by the algorithm if the latter was allowed to continue further.

## 5 Conclusions

The proposed algorithm can be viewed as extension of the Berlekamp-Massey algorithm [1] and the FIA scheme [2]. The updates are based on a discrepancy detection mechanism but the search is carried over a multidimensional set ordered by the total degree order plus a cost function reflecting the implementation cost. Although the proposed algorithm is designed to produce the minimal architecture, it can also be employed to determine alternate architectures that generate the given sequence. Furthermore the proposed algorithm applies to sequences in arbitrary fields.

## References

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